EAST Search History

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
L2	2	(plurality many various multiple) with (region compartment section partition arrays) with (CMOS near (data setting RAM configurat$3 memory)) and (backup mirror$3 imag$3) with (CMOS near (data setting RAM configurat$3 memory))	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2008/05/07 16:57
L3	104	(plurality many various multiple) with (region compartment section partition arrays) and (CMOS near (data setting RAM configurat$3 memory)) and (backup mirror$3 imag$3) with (CMOS near (data setting RAM configurat$3 memory)) and (authenticat$3 verify$3 validity) with (mask checksum CRC encrypt$3)	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2008/05/07 17:02
L4	0	(plurality many various multiple) with (region compartment section partition arrays) and (CMOS near (data setting RAM configurat$3 memory)) and (backup mirror$3 imag$3) with (CMOS near (data setting RAM configurat$3 memory)) and (region compartment section partition arrays) with	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2008/05/07 17:03

		(authenticat$3 verify $3 validity) with (mask checksum CRC encrypt$3)				
L5	22	(plurality many various multiple) with (region compartment section partition arrays) and (CMOS near (data setting RAM configurat$3 memory)) and (backup mirror$3 imag$3) with (CMOS near (data setting RAM configurat$3 memory)) and (authenticat$3 verify $3 validity) with (mask checksum CRC encrypt$3) and @ay<="2001"	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2008/05/07 17:04
L6	0	(plurality many various multiple) with (region compartment section partition arrays) with (CMOS near (data setting RAM configurat$3 memory) with (stor $3 sav$3)) and (backup mirror$3 imag$3) with (CMOS near (data setting RAM configurat$3 memory)) and (authenticat$3 verify $3 validity) with (mask checksum CRC encrypt$3) and @ay<="2001"	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2008/05/07 17:05
L7	0	(CMOS with (region section partition array compartment) with (validat$3 validity authenticat$3 authenticity verification varify$3) and (backup mirror $3 imag$3)) and 713/2.ccls.	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2008/05/07 17:05

L8	1	(intel near corporation).as. and ((cmos with bios) and (backup mirror $3 imag$3)).clm.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2008/05/07 17:05
L9	1	(CMOS adj (data setting RAM configurat$3 memory)) with (several plurality many various divid$3 split$4 section$3) near5 (compartment region section partion array) and (secur$3 protect$3 athenticat$3) with (mask$3 CRC checksum encrypt$3 hash$3)	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2008/05/07 17:06
L10	0	CMOS with (region section partition array compartment) with (validat$3 validity authenticat$3 authenticity verification varify$3) and (CMOS near (data setting RAM configurat$3 memory)) with (backup mirror$3 imag$3)	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2008/05/07 17:07
L11	0	((CMOS adj (data setting RAM configurat$3 memory)) with (several plurality many various divid$3 split$4 section$3) near5 (compartment region section partion array) and (secur$3 protect$3 athenticat$3) with (mask$3 CRC checksum encrypt$3 hash$3) and (plurality many various multiple) with (region compartment	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2008/05/07 17:07

		section partition arrays) and (CMOS near (data setting RAM configurat$3 memory)) and (backup mirror$3 imag$3) with (CMOS near (data setting RAM configurat$3 memory)) and (authenticat$3 verify $3 validity) with (mask checksum CRC encrypt$3)					
L12	0	(CMOS with (region section partition array compartment) with (validat$3 validity authenticat$3 authenticity verification varify$3) and (backup mirror $3 imag$3)) and 713/2.ccls.	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2008/05/07 17:07
L13	0	(CMOS with (region section partition array compartment) with (validat$3 validity authenticat$3 authenticity verification varify$3) and (backup mirror $3 imag$3)) and 713/190.ccls.	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2008/05/07 17:08
L14	0	(CMOS with (region section partition array compartment) with (validat$3 validity authenticat$3 authenticity verification varify$3) and (backup mirror $3 imag$3)) and 713/189.ccls.	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2008/05/07 17:08

L15	0	(CMOS near (data setting RAM configurat$3 memory)) with (backup mirror$3 imag$3) and (CMOS near (data setting RAM configurat$3 memory)) with (compartment region divid$3 split$3 section partion array) and 713/189.ccls.	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2008/05/07 17:08
L16	1	(plurality many various multiple) near3 (region compartment section partition arrays) with (CMOS near (data setting RAM configurat$3 memory)) and (backup mirror$3 imag$3) with (CMOS near (data setting RAM configurat$3 memory))	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2008/05/07 17:08
L17	0	(plurality many various multiple) near3 (region compartment section partition arrays) with (CMOS near (data setting RAM configurat$3 memory)) and (backup mirror$3 imag$3) with (CMOS near (data setting RAM configurat$3 memory)) and (authenticat$3 verify $3 validity) with (mask checksum CRC encrypt$3)	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2008/05/07 17:09

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